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DIRECT MAPPED CACHE ORGANIZATION

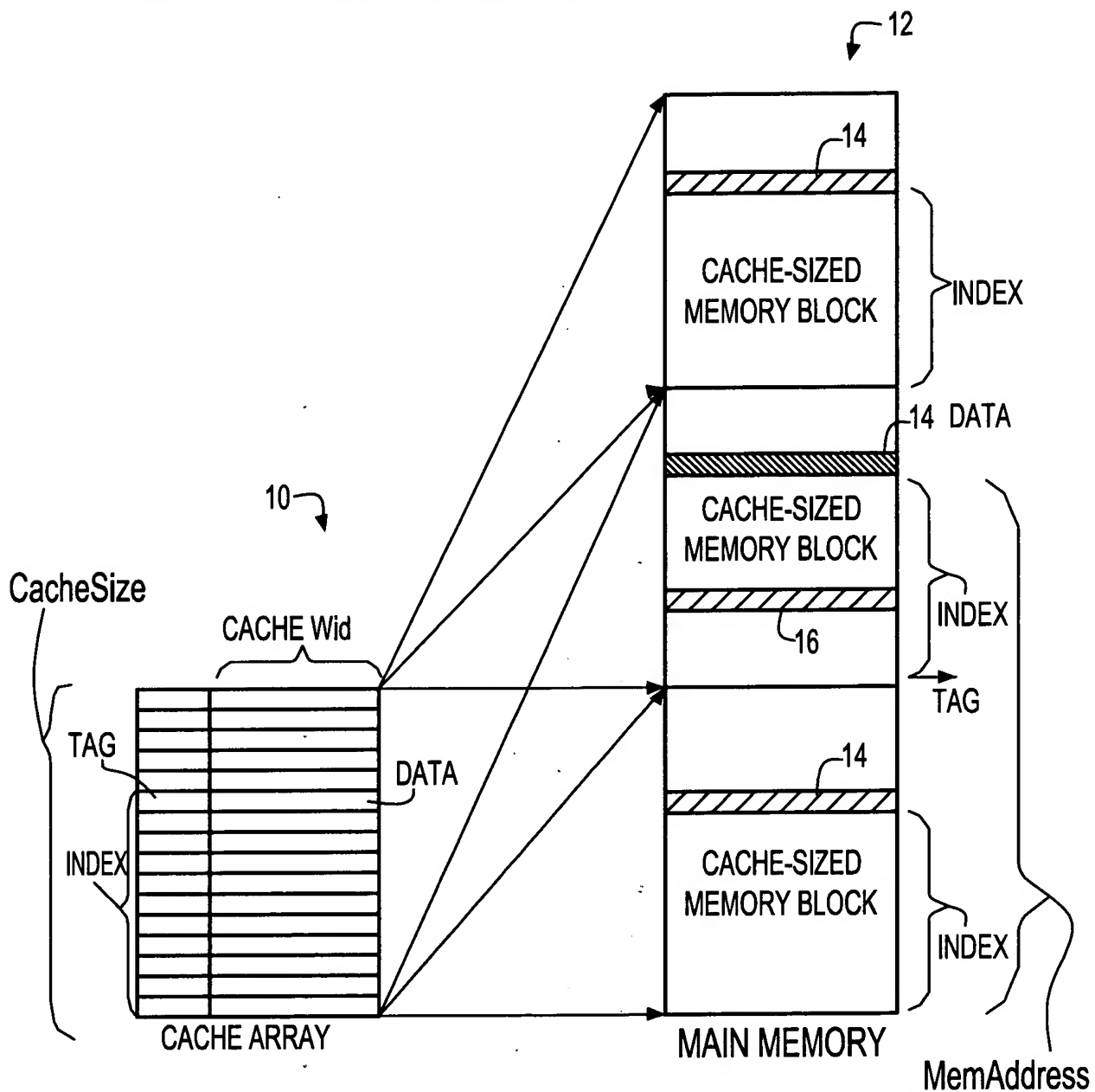


FIG. 1

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DIRECT MAPPED CACHE 1/2 POWER

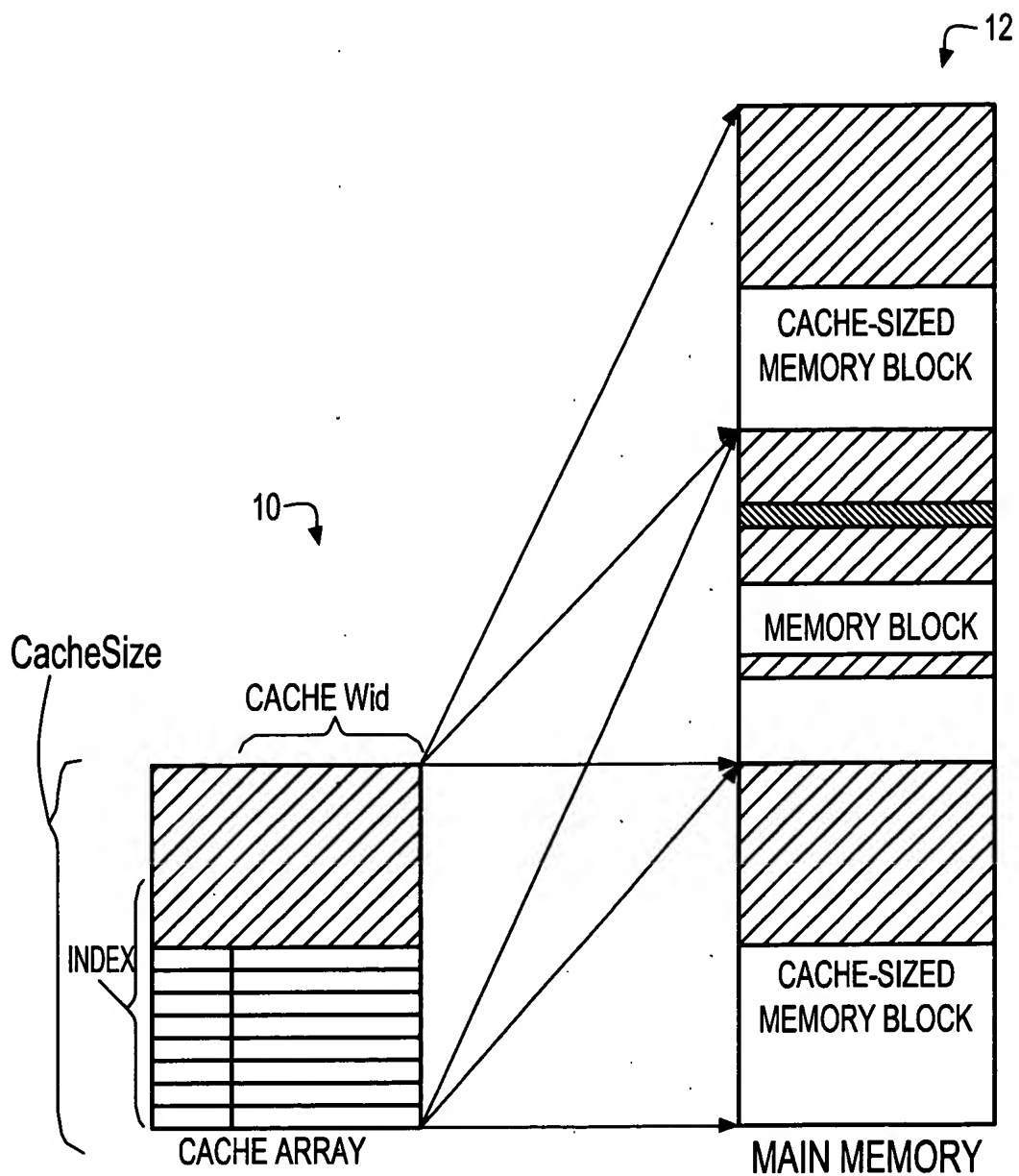


FIG. 2

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DIRECT MAPPED CACHE 1/2 POWER

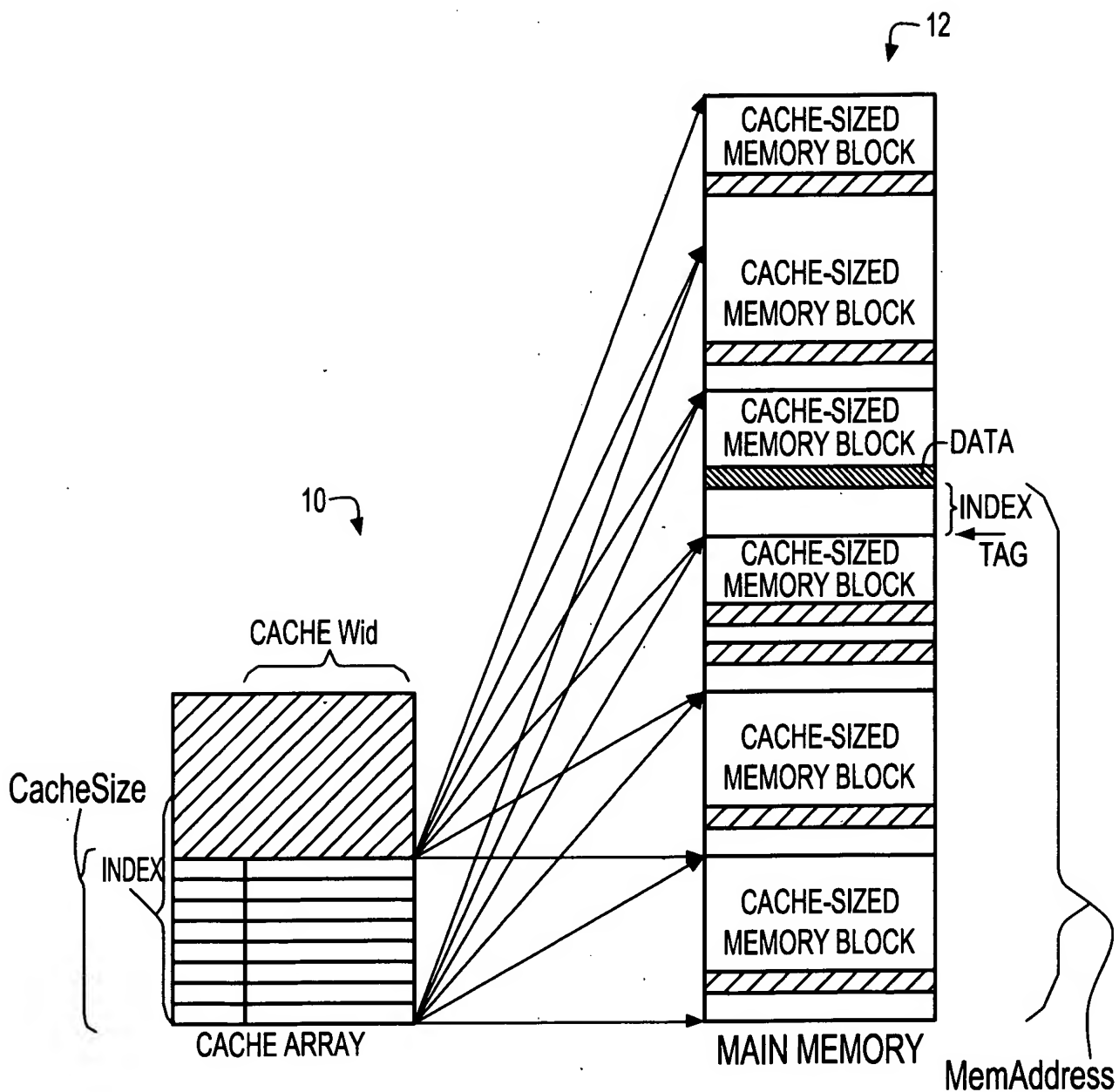


FIG. 3

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2-WAY SET ASSOCIATIVE CACHE ORGANIZATION

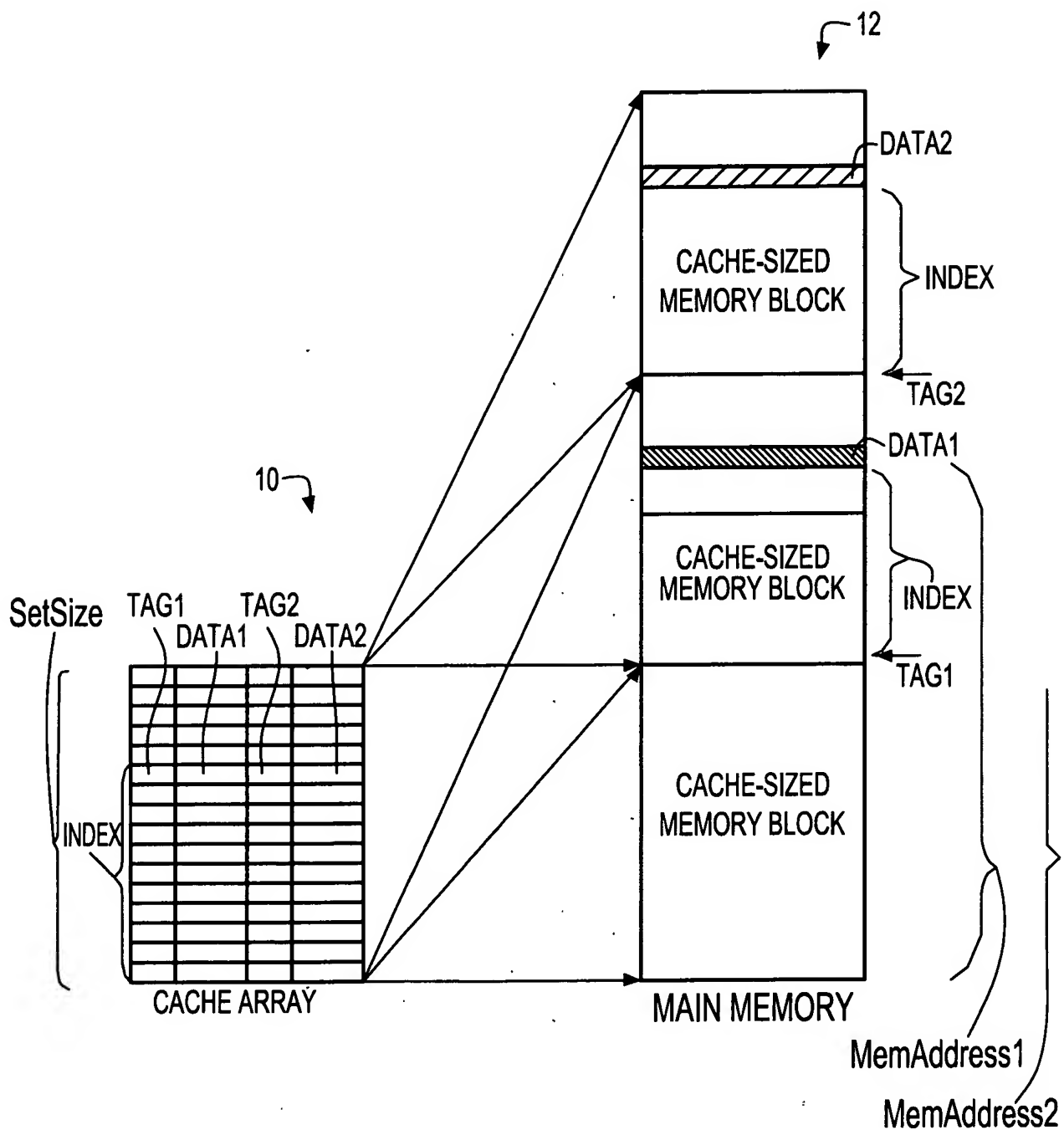
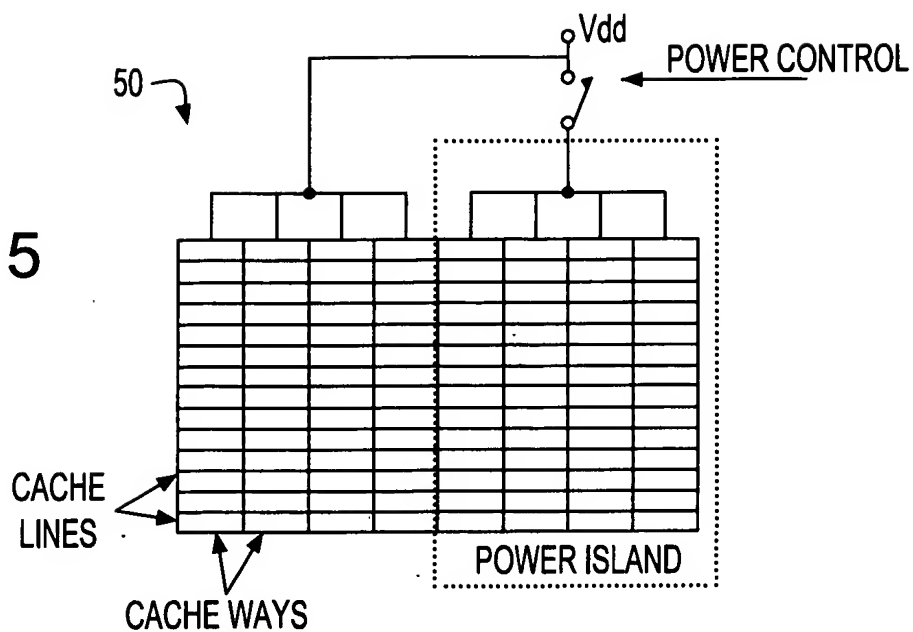


FIG. 4

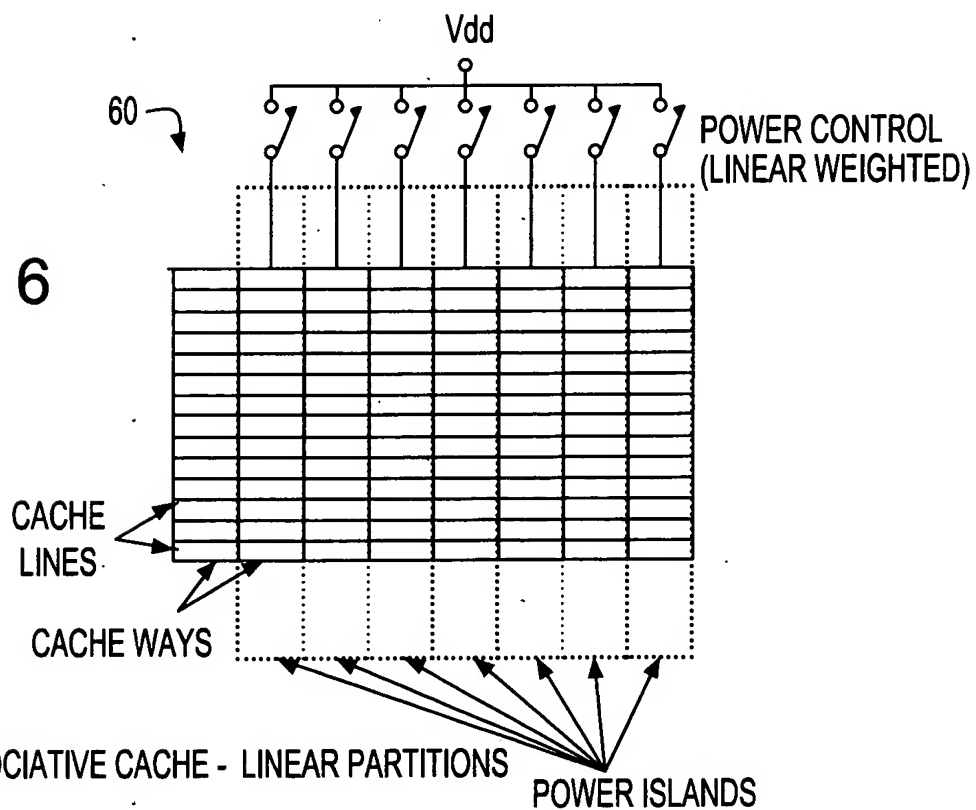
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FIG. 5



8 WAY ASSOCIATIVE CACHE - 1/2 CACHE POWER SAVING

FIG. 6



8 WAY ASSOCIATIVE CACHE - LINEAR PARTITIONS

